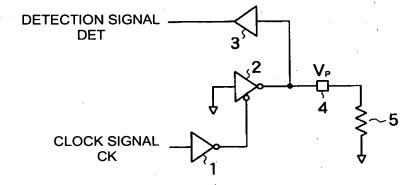
Title: CLOCK SIGNAL DETECTION CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT

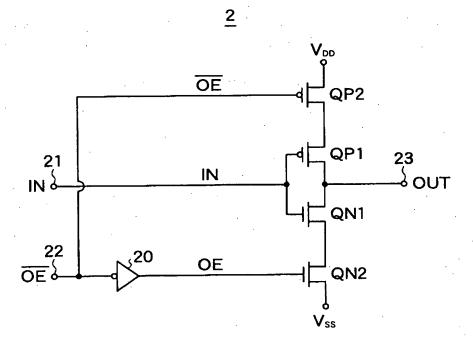
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## FIG.1



## FIG.2



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FIG.3

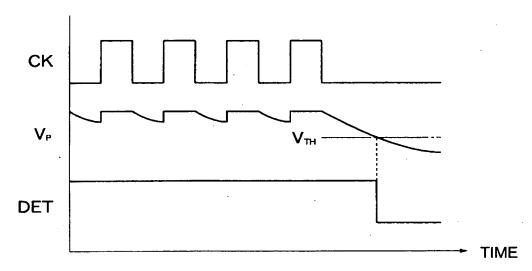
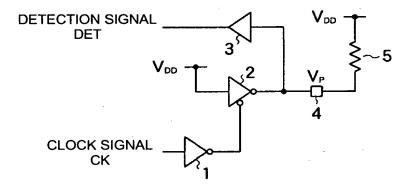


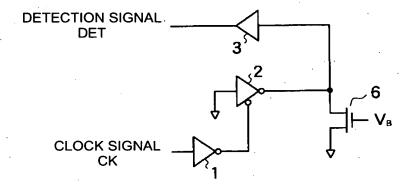
FIG.4



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## FIG.5



## FIG.6

